

<b>Notice of References Cited</b>	Application/Control No. 10/632,024		Applicant(s)/Patent Under Reexamination CHAUVEL ET AL.	
	Examiner Jeffrey R. Swearingen		Art Unit 2145	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,524,250	06-1996	Chesson et al.	712/228
*	B	US-5,724,564	03-1998	Conder et al.	712/234
*	C	US-5,872,963	02-1999	Bitar et al.	712/233
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	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Qiu, Xiaogang et al. "Tolerating Late Memory Traps in ILP Processors." Proceedings of the 26th Annual International Symposium on Computer Architecture. IEEE Computer Society. May 1999. pp 76-87
	V	Hansen, Per Brinch. Operating System Principles. Prentice-Hall, Inc. January 1973. 384 pages
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.